	ISSUE CLASSIFICATION	
Subclass		
Class		

PATENT NUMBER

JC1
5C555

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED Q.A.	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/724972		365		2818 2137	

APPLICANTS

Stephen Trimberger
Raymond Pang
Walter Sze
Jennifer Wong

TITLE

Programmable logic device with decryption and structure for preventing design relocation

PTO-2040
12/99

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ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								
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TERMINAL DISCLAIMER ☐	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	(Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
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